# **Refine Search**

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L2 and L24	7

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DB=P	GPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES;	OP = OR	
<u>L27</u>	12 and 124	7	<u>L27</u>
<u>L26</u>	12 and L25	0	<u>L26</u>
<u>L25</u>	110 and L24	0	<u>L25</u>
<u>L24</u>	13 and 16 and 123	155	<u>L24</u>
<u>L23</u>	(116 or 117 or 118 or 119 or 120 or 121 or 122)	1964	<u>L23</u>
<u>L22</u>	MATSUBARA-KEN\$.in.	843	<u>L22</u>
<u>L21</u>	FUJISAWA-TOMOYUKI\$.in.	42	<u>L21</u>
<u>L20</u>	TAMURA-TAKAYUKI\$.in.	277	<u>L20</u>
<u>L19</u>	NOZOE-ATSUSHI\$.in.	100	<u>L19</u>
<u>L18</u>	HORII-TAKASHI\$.in.	166	<u>L18</u>
<u>L17</u>	YOSHIDA-KEIICHi\$.in.	610	<u>L17</u>
<u>L16</u>	TAKASE-YOSHINORI\$.in.	33	<u>L16</u>
<u>L15</u>	12 and L14	0	<u>L15</u>
<u>L14</u>	110 and L13	19	<u>L14</u>

<u>L13</u>	initializ\$ near3 13	4675	<u>L13</u>
<u>L12</u>	17 and 18	2	<u>L12</u>
<u>L11</u>	17 and 18 and L10	0	<u>L11</u>
<u>L10</u>	13 near3 L9	247	<u>L10</u>
<u>L9</u>	(outside or external) near2 access	33297	<u>L9</u>
<u>L8</u>	16 near2 14 near3 15	252	<u>L8</u>
<u>L7</u>	13 near2 14 near3 15	645	<u>L7</u>
<u>L6</u>	(nonvolatile or (non-volatile) or flash) adj2 memory	245789	<u>L6</u>
<u>L5</u>	area or region	5146770	<u>L5</u>
<u>L4</u>	divid\$	1931955	<u>L4</u>
<u>L3</u>	buffer	904402	<u>L3</u>
<u>L2</u>	711/103.ccls.	2009	<u>L2</u>
<u>L1</u>	510150.ap.	8	<u>L1</u>

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### Search Results -

Terms	Documents	
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Search:

L15

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<u>L15</u>	12 and L14	0	<u>L15</u>
<u>L14</u>	110 and L13	19	<u>L14</u>
<u>L13</u>	initializ\$ near3 13	4675	<u>L13</u>
<u>L12</u>	17-and 18	2	<u>L12</u>
<u>L11</u>	17 and 18 and L10	0	<u>L11</u>
<u>L10</u>	13 near3 L9	247	<u>L10</u>
<u>L9</u>	(outside or external) near2 access	33297	<u>L9</u>
<u>L8</u>	16 near2 14 near3 15	252	<u>L8</u>
<u>L7</u>	13 near2 14 near3 15	645	<u>L7</u>
<u>L6</u>	(nonvolatile or (non-volatile) or flash) adj2 memory	245789	<u>L6</u>
<u>L5</u>	area or region	5146770	<u>L5</u>
<u>L4</u>	divid\$	1931955	<u>L4</u>
<u>L3</u>	buffer	904402	<u>L3</u>
<u>L2</u>	711/103.ccls.	2009	<u>L2</u>

<u>L1</u>. 510150.ap.

8 <u>L1</u>

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# Freeform Search

	Database:	US Patents Full-TO EPO Abstracts JPO Abstracts I Derwent World	Database Database	atabase	
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L8	17 and L6			5216	L8
<u>L8</u> L7	12 and 16			113	<u>L7</u>
<u>L6</u>	13 near3 14		•	994	L6
L5	buffer or cacl	1e.		976664	<u>L5</u>
<u>23</u> L4	bank			253258	L4
L3		or non-volatile	or flash) adj2 memo		<u>L3</u>
<u>L2</u>	711/103.ccls.		, 3	2009	<u>L2</u>
L1	20060104112	2		2	L1

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((((((nonvolatile <or> flash <and> memory)<in>metadata))<and>(buffer<in>metadata)

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1. Flash memory electromagnetic compatibility

Fogle, A.D.; Don Darling; Blish, R.C., II; Daszko, G.;

Device and Materials Reliability, IEEE Transactions on

Volume 6, Issue 2, June 2006 Page(s):266 - 276 Digital Object Identifier 10.1109/TDMR.2006.876573

AbstractPlus | Full Text: PDF(1624 KB) | IEEE JNL

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 $\Gamma$ 2. High density radiation hardened FeRAMs on a 130 nm CMOS/FRAM process

Kamp, D.A.; DeVilbiss, A.D.; Haag, G.R.; Russell, K.E.; Derbenwick, G.F.;

Non-Volatile Memory Technology Symposium, 2005

7-10 Nov. 2005 Page(s):4 pp.

Digital Object Identifier 10.1109/NVMT.2005.1541393

AbstractPlus | Full Text: PDF(234 KB) IEEE CNF

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3. The flash memory read path: building blocks and critical aspects 

Micheloni, R.; Crippa, L.; Sangalli, M.; Carnpardo, G.;

Proceedings of the IEEE

Volume 91, Issue 4, April 2003 Page(s):537 - 553

Digital Object Identifier 10.1109/JPROC.2003.811704

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4. A 6-ns cycle 256-kb cache memory and memory management unit

Heald, R.A.; Holst, J.C.;

Solid-State Circuits, IEEE Journal of

Volume 28, Issue 11, Nov. 1993 Page(s):1078 - 1083

Digital Object Identifier 10.1109/4.245585

AbstractPlus | Full Text: PDF(476 KB) IEEE JNL

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2770

#2 ((((nonvolatile <or> flash <and> memory)<in>metadata))<AND> (buffer<in>metadata))

59

(((((((nonvolatile <or> flash <and> memory)<in>metadata))<and>
(buffer<in>metadata)))<AND>((outside or independent) <and>
access\*<in>metadata))

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